



# 2016 International Conference on Analog VLSI Circuits August 24-26, 2016, Boston, USA



## General Chair

Katsu Nakamura, Analog Devices, Inc.

## General Co-Chair

Akira Hyogo, Tokyo University of Science

## Technical Program Committee Chair

Toshihiko Hamasaki, Hiroshima Institute of Technology

## Technical Program Committee Secretaries

Hiroo Sekiya, Chiba University  
Takahide Sato, University of Yamanashi

## Publication and Publicity Co-chairs

Hao San, Tokyo City University  
Tomochika Harada, Yamagata University

## Local Chair

Yukihisa Handa, Analog Devices, Inc.

## Committee Liaison

Takeshi Shima, Kanagawa University  
Hiroki Sato, Tokyo Institute of Technology  
Nicodimus Retdian, Kanagawa University

## Important Dates

Paper Submission Deadline:

**April 30, 2016**

Author Notification Due:

**June 13, 2016**

Registration Starts:

**June 30, 2016**

Final Paper Submission Deadline:

**July 13, 2016**

Conference:

**August 24-26, 2016**

## Contact:

info@avic2016.ieeej-ect.org

<https://avic2016.ieeej-ect.org>

## Scope

The Research Committee on Electronic Circuits of the Institute of Electrical Engineers of Japan (IEEJ) in cooperation with Analog Devices Inc. will hold 2016 International Conference on Analog VLSI Circuits (AVIC) in Boston, USA, on August 24-26, 2016.

AVIC is the successor of International Analog VLSI Workshop, which had been held for 18 years. The purpose of this conference is to discuss state-of-the-art ideas and results from researches on analog VLSI circuits and their applications. It has contributed to long-standing worldwide community of analog VLSI circuit experts in industry and academia.

## Topics

AVIC 2016 covers topics related to analog and mixed-signal integrated circuits including but not limited to:

- Analog and Mixed-signal Integrated Circuits:
  - Amplifiers, Filters, Comparators, Oscillators, Multipliers, PLLs, RF ICs
  - Voltage/Current References, Sample-And-Hold Circuits
  - A/D and D/A Converters, High Speed IO Interface
  - Energy Harvesting, Power Management
- Analog VLSI Design Automation:
  - Layout, Simulation, HDL, Analog IP
- Signal/Information Processing and Applications:
  - Communication, Multimedia, Automotive Electronics
  - Biomedical Electronics, Consumer Electronics
  - Non-linear Signal Processing, Neural Network
  - Sensing and Sensor Networks, Space Electronics
- Digital Circuits and Digital Signal Processing

## Paper Submission

Paper submission is accepted via the official website <https://avic2016.ieeej-ect.org>. Authors are required to send a paper in English within 6 pages, in PDF format, along with information including paper title, author name(s), affiliation(s) of author(s), and the corresponding author (name, postal and e-mail addresses). Notification will be sent to the authors via e-mail. Accepted papers must be resubmitted as the final (camera-ready) ones. At least one of the authors of a paper must register and give a presentation at the conference.

Visit AVIC 2016 website for additional information: <https://avic2016.ieeej-ect.org>.

## Conference Venue

Cambridge Innovation Center (CIC)

One Broadway, 5th floor

Cambridge, MA 02142

<http://cic.us/>

**Selected papers will be published in a SPRINGER journal special issue**